ABSTRACT OF THE DISCLOSURE

A disk device for performing error-correction

processing is loaded with a high-reliability disk to which a
C1 code as a low-level error-correction code (ECC) is added
for every sector, and a C2 code as a high-level ECC is added
for every plurality of sectors. A host computer includes a
device driver corresponding to the high-reliability disk.

When data written to the high-reliability disk is read, lowlevel error-correction is performed based on the C1 code in
units of one block corresponding to one sector, and the
corrected data is notified to the host computer.

Simultaneously, the C2 code along with the data for a
plurality of blocks is loaded, and high-level error
correction based on the C2 code is thereby performed.